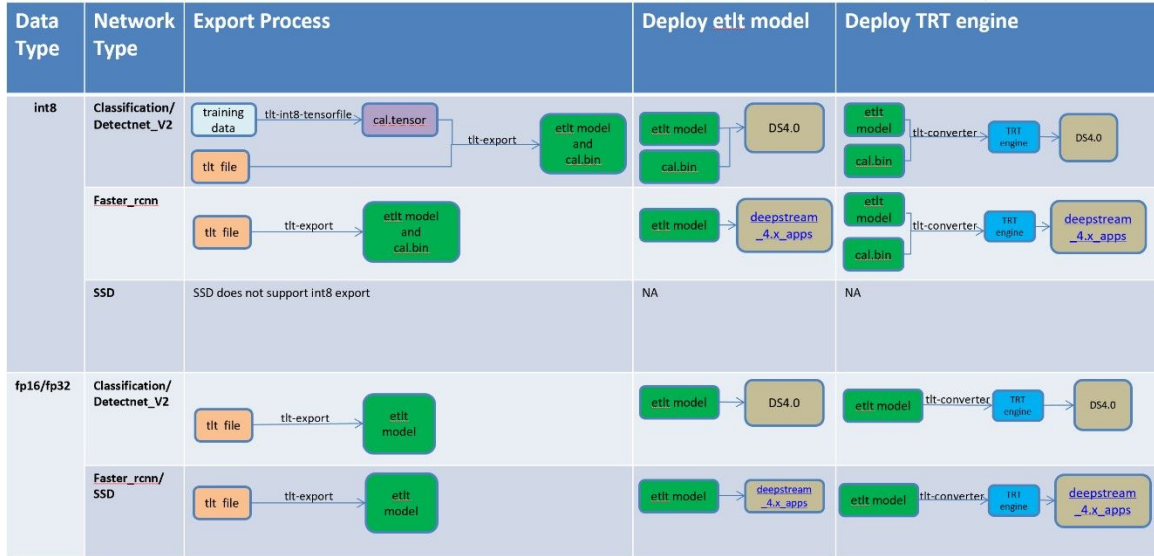
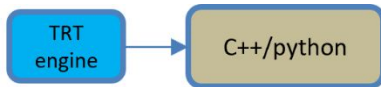


- Below figure shows the current process of export and deployment. The first method shows how to deploy etlt model to DS4.0 or [https://github.com/NVIDIA-AI-IOT/deepstream\\_4.x\\_apps](https://github.com/NVIDIA-AI-IOT/deepstream_4.x_apps). The second method shows how to generate TRT engine and deploy it to DS4.0 or [https://github.com/NVIDIA-AI-IOT/deepstream\\_4.x\\_apps](https://github.com/NVIDIA-AI-IOT/deepstream_4.x_apps).



- Below figure shows a new deploy method for TRT engine. Note: One TRT engine is only runnable on NVGPU.



Please refer to preprocess/postprocess code which is exposed in C++ in

- nvdsinfer\_customparser\_frcnn\_uff folder from [https://github.com/NVIDIA-AI-IOT/deepstream\\_4.x\\_apps](https://github.com/NVIDIA-AI-IOT/deepstream_4.x_apps)
- nvdsinfer\_customparser\_ssd\_uff folder from [https://github.com/NVIDIA-AI-IOT/deepstream\\_4.x\\_apps](https://github.com/NVIDIA-AI-IOT/deepstream_4.x_apps)
- deepstream\_sdk\_v4.0.1\_jetson/sources/libs/nvdsinfer\_customparser/nvdsinfer\_custombboxparser.cpp from <https://developer.nvidia.com/deepstream-download>